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TRANSMITTAL FORM

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		Application Number	10/671,483
		Filing Date	September 29, 2003
		First Named Inventor	Michihiko YANAGISAWA
		Art Unit	1763
		Examiner Name	J. R. Lund
Total Number of Pages in This Submission	5	Attorney Docket Number	506212001200

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	MORRISON & FOERSTER LLP		
Signature			
Printed name	Barry E. Bretschneider		
Date	June 7, 2005	Reg. No.	28,055



PATENT
Docket No. 506212001200

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Michihiko YANAGISAWA *et al.*

Serial No.: 10/671,483

Filing Date: September 29, 2003

For: MULTI-STEP DRY ETCHING
METHOD FOR SOI WAFER

Examiner: J. R. Lund

Group Art Unit: 1763

RESPONSE UNDER 37 CFR 1.111

M/S Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In response to the Action mailed March 9, 2005, please reconsider this application in light of the following Remarks.

REMARKS

Claims 5-17 have been rejected under 35 USC 103(a) as unpatentable over Japanese Patent Application Publication No. Hei. 5-29262 (Nishibe) in view of U.S. Patent No. 6,302,995 (Tanaka) and U.S. Patent No. 6,315,858 (Shinozuka). Applicants respectfully traverse this rejection.

Claim 5 recites a transportation device for taking a silicon on insulator wafer after completion of planarization processing out from the first chamber and transporting the silicon on insulator wafer into the second chamber. Because the claimed etching apparatus has the first vacuum chamber having the small diameter nozzle and the second vacuum chamber